

## REMARKS

Claims 1-12 are rejected. Claims 1-12 are pending.

The specification has been corrected to provide clarity and consistency in disclosing the present invention.

Claims 1, 3-7 stand rejected under 35 U.S.C. §102(e) as allegedly being anticipated by Doh et al (US 6,909,082). Applicant respectfully traverses the rejections.

Claim 1, as amended, recites, *inter alia*, "...a peak hold capacitor for charging a peak value and **maintaining a peak level** when the transistor received the positive signal; a signal amplitude detector to monitor the received **burst signal** amplitude **and generates a control signal corresponding to the signal amplitude...**"

The present invention teaches an optical receiver capable of automatically adjusting a current according to the signal amplitude byway of a peak hold capacitor and maintaining a peak level and a bottom level in a peak detector circuit (See, Page 9, line 8 to 19, FIG. 5 (Peak Detector)). As recited in the base claim, the present invention monitors a burst signal and outputs a control signal corresponding to the signal amplitude (or size).

In contrast, Doh et al. teaches an optical receiver which adjusts the current by manipulating the RC time constant, specifically the size of the capacitor (See. Column 8, line 13 to 20), thus Doh et al. teaches a method of detecting a signal size from a signal size detector. Accordingly, Doh et al. fails to show that the peak holder capacitor maintains a peak level and that the signal detector monitors a burst signal amplitude and generates a control signal corresponding to the signal amplitude, as recited in claim 1, applicant respectfully submits that Doh does not anticipate the present invention.

Claims 8 -12 stand rejected under 35 U.S.C. § 103(a) as allegedly unpatentable over Doh et al. (US 6,909,082) in view of Nagahori. Applicant respectfully traverses the rejections.

Applicant respectfully submits that Doh et al. was filed in the USPTO on March 25, 2003 and the present invention was filed in the USPTO on October 23, 2003 claiming a foreign priority date of June 11, 2003, and that both applications are commonly assigned to Samsung Electronics Co., Ltd. at the time of filing. As the instant application was filed on or after November 22, 2000, the rejection using Doh et al. is invalid.

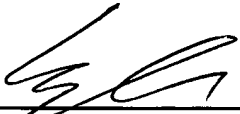
Further, claim 8 has been amended to recite, *inter alia*, "... peak hold capacitor [which] charges a bottom value when the diode is turned on and **maintains the bottom level**; a signal amplitude detector to monitor the received **burst signal** amplitude and **generates a control signal corresponding to the signal amplitude** . . ."

As stated above, claim 8, as amended, now recites features not disclosed by Doh et al. and Naghori, alone or in combination, thus does not render the present invention obvious.

The other claims in this application are each dependent from the independent claim discussed above and are therefore believed patentable for the same reasons. Since each dependent claim is also deemed to define an additional aspect of the invention, however, the individual consideration of the patentability of each on its own merits is respectfully requested.

For all the foregoing reasons, it is respectfully submitted that all the present claims are patentable in view of the cited reference. A notice of Allowance is respectfully requested.

Respectfully submitted,

  
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STEVE S. CHA  
Attorney for Applicant(s)  
Registration No. 44,069


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CHA & REITER, LLC  
210 Route 4 East #103  
Paramus, NJ 07652  
(201) 226-9245

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Steve Cha, Reg. No. 44,069  
\_\_\_\_\_  
(Name of Registered Rep.)

  
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(Signature and Date)